

Abstract

Jovanovic D.S., B. Orlic, J.F. Broenink and J. van Amerongen, *"Inexpensive prototyping for mechatronic systems"*, Proceedings WESIC 2003, WESIC 2003, May 28-30, 2003, University of Miskolc, Hungary, pp 431-438, ISBN 963-661-570-5, 2003

This paper provides a description of an approach based on our 20-sim modelling and simulation PC software package in combination with various hardware targets with an Analog Devices Inc. DSP board as an instance. The main virtue of this approach is support for a number of (virtually any decent) hardware platforms for, in this case, a price less than a few percent compared to existing market solutions.